

Notice of Allowability

Application No.

10/506,947

Examiner

Tu Ba Hoang

Applicant(s)

KOBAYASHI, EIJI

Art Unit

2832

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address--

All claims being allowable, PROSECUTION ON THE MERITS IS (OR REMAINS) CLOSED in this application. If not included herewith (or previously mailed), a Notice of Allowance (PTOL-85) or other appropriate communication will be mailed in due course. **THIS NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT RIGHTS.** This application is subject to withdrawal from issue at the initiative of the Office or upon petition by the applicant. See 37 CFR 1.313 and MPEP 1308.

1. ☒ This communication is responsive to communication filed 03/22/06.
2. ☒ The allowed claim(s) is/are 1-2 and 5-6.
3. ☐ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
- a) ☐ All b) ☐ Some* c) ☐ None of the:
1. ☐ Certified copies of the priority documents have been received.
2. ☐ Certified copies of the priority documents have been received in Application No. _____.
3. ☐ Copies of the certified copies of the priority documents have been received in this national stage application from the International Bureau (PCT Rule 17.2(a)).

* Certified copies not received: _____.

Applicant has THREE MONTHS FROM THE "MAILING DATE" of this communication to file a reply complying with the requirements noted below. Failure to timely comply will result in ABANDONMENT of this application.

THIS THREE-MONTH PERIOD IS NOT EXTENDABLE.

4. ☐ A SUBSTITUTE OATH OR DECLARATION must be submitted. Note the attached EXAMINER'S AMENDMENT or NOTICE OF INFORMAL PATENT APPLICATION (PTO-152) which gives reason(s) why the oath or declaration is deficient.
5. ☒ CORRECTED DRAWINGS (as "replacement sheets") must be submitted.
- (a) ☐ including changes required by the Notice of Draftsperson's Patent Drawing Review (PTO-948) attached
- 1) ☐ hereto or 2) ☐ to Paper No./Mail Date _____.
- (b) ☒ including changes required by the attached Examiner's Amendment / Comment or in the Office action of Paper No./Mail Date _____.
- Identifying indicia such as the application number (see 37 CFR 1.84(c)) should be written on the drawings in the front (not the back) of each sheet. Replacement sheet(s) should be labeled as such in the header according to 37 CFR 1.121(d).
6. ☐ DEPOSIT OF and/or INFORMATION about the deposit of BIOLOGICAL MATERIAL must be submitted. Note the attached Examiner's comment regarding REQUIREMENT FOR THE DEPOSIT OF BIOLOGICAL MATERIAL.

Attachment(s)

1. ☐ Notice of References Cited (PTO-892)
2. ☐ Notice of Draftsperson's Patent Drawing Review (PTO-948)
3. ☒ Information Disclosure Statements (PTO-1449 or PTO/SB/08),
Paper No./Mail Date 3/22/06&6/21/06
4. ☐ Examiner's Comment Regarding Requirement for Deposit of Biological Material
5. ☐ Notice of Informal Patent Application (PTO-152)
6. ☐ Interview Summary (PTO-413),
Paper No./Mail Date _____.
7. ☒ Examiner's Amendment/Comment
8. ☒ Examiner's Statement of Reasons for Allowance
9. ☐ Other _____.

Tu Hoang
PRIMARY EXAMINER
AU2832

EXAMINER'S AMENDMENT

An examiner's amendment to the record appears below. Should the changes and/or additions be unacceptable to applicant, an amendment may be filed as provided by 37 CFR 1.312. To ensure consideration of such an amendment, it MUST be submitted no later than the payment of the issue fee.

Authorization for this examiner's amendment was given in a telephone interview with G.Gregory Schivley on July 17, 2006.

The application has been amended as follows:

In the claim, replace claims 1 and 2 and amend claims 5 and 6 as follow:

1. (currently amended) A surface mounting chip network component having an even number of network circuits formed on the surface of a rectangular insulating substrate, each of the network circuits having three or more odd number of terminals[,] and comprising an equal number of said terminals arranged at each of [the] its facing sides on said insulating substrate, and elements of said network circuits formed on both faces of said insulating substrate;
wherein each [an] individual of said network circuits has a common conductor extended from a terminal on a side of said insulating substrate, and [one or more elements selected from:]
 - (a) a resistor connected to said common conductor at one of its [an] edges;
 - (b) another terminal on a side of said insulating substrate to which the other edge of said resistor is connected;
 - (c) another resistor formed on the reverse side with respect to the face of the

insulating substrate where the resistor of (a) is formed, [an] one edge of [the] said another resistor connected to the terminal of (b); and
(d) an independent terminal connected to the other edge of the resistor of (c),
the independent terminal located on the opposite side with respect to the side of said insulating substrate where the terminal of (b) is arranged; and wherein all of said network circuits formed on said insulating substrate are equivalent and the terminals of at least [the corresponding] two network circuits are arranged point-symmetrically with respect to a center of the surface of said insulating substrate.

2. (Currently amended) A surface mounting chip network component according to claim 1, wherein said terminals are arranged on all of four sides [,] of each of said network circuits with the number of terminals on a side being equal to one on the opposite side and the number of terminals on one of the other remaining two opposite sides being equal to the other.

In claim 5, replace "the faces" at line 2 with - - said both faces - -.

In claim 6, at line 3, before "both faces", insert the word - - said - -.

The following changes to the drawings have been approved by the examiner and agreed upon by applicant: Figure 3 has been labeled as PRIOR ART. In order to avoid abandonment of the application, applicant must make these above agreed upon drawing changes.

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The following is an examiner's statement of reasons for allowance: allowance of claims 1-2 and 5-6 is indicated because the prior art of record does not show or fairly suggest the common conductor in the manner recited in claim 1.

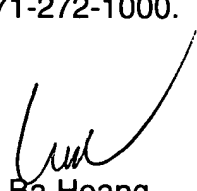
Any comments considered necessary by applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably accompany the issue fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for Allowance."

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Tu Ba Hoang whose telephone number is (571) 272-4780. The examiner can normally be reached on Mon-Thu from 6:00AM to 6:30PM.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Elvin Enad can be reached on (571) 272-1990. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

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Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free). If you would like assistance from a USPTO Customer Service Representative or access to the automated information system, call 800-786-9199 (IN USA OR CANADA) or 571-272-1000.



Tu Ba Hoang
Primary Examiner
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July 24, 2006

Fig.3 (PRIOR ART)

